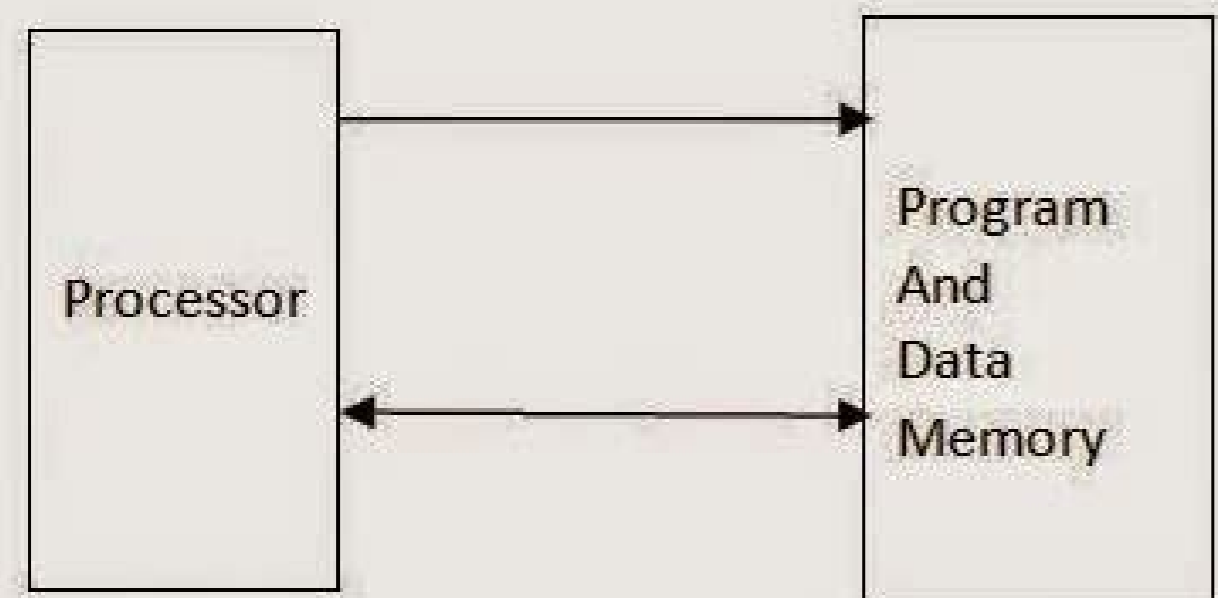
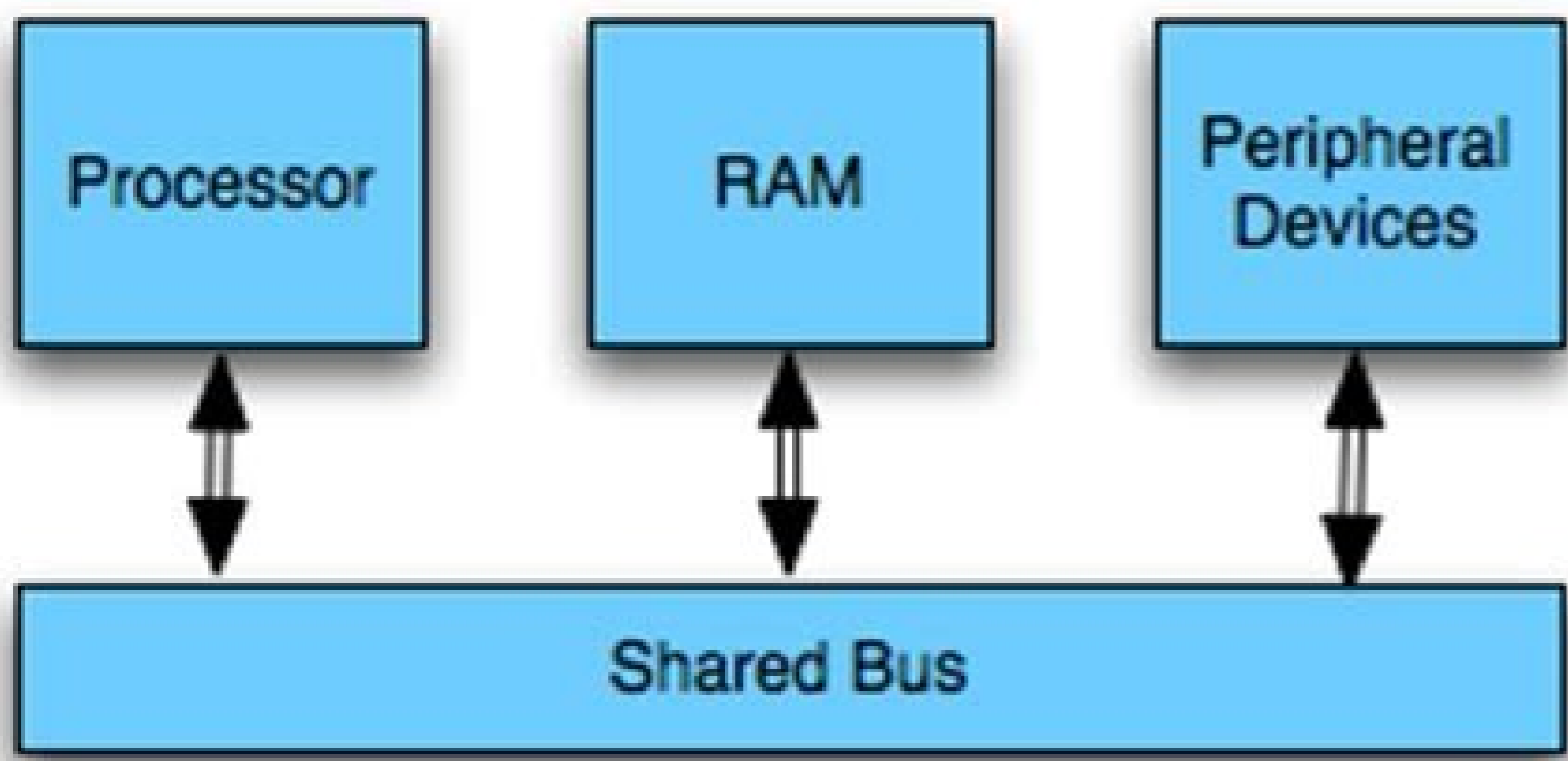
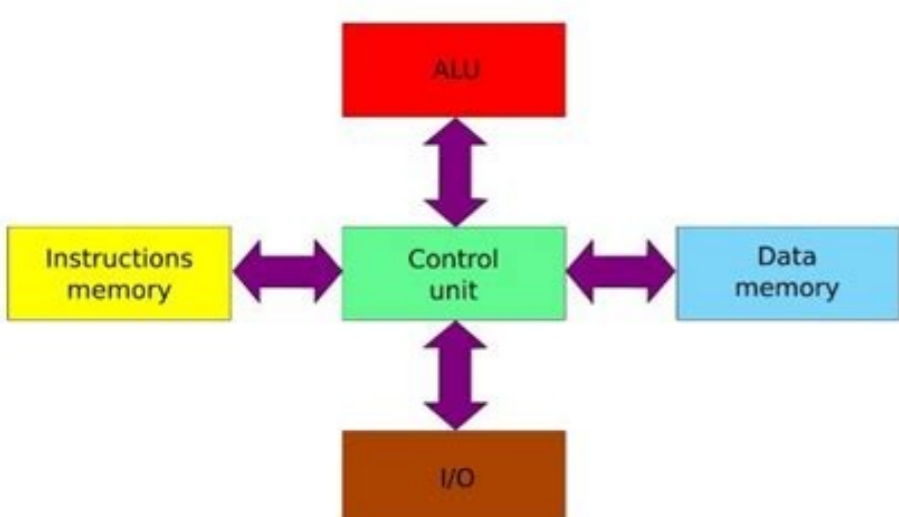


I'm not robot!

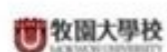


VON NEUMANN ARCHITECTURE	
HARVARD ARCHITECTURE	
Has a shared bus between the processor and memory.	Has separate buses for instructions and data.
Has a single memory for both instructions and data.	Has separate memory for instructions and data.
Has a single bus for both instructions and data.	Has separate buses for instructions and data.
Has a single bus for both instructions and data.	Has separate buses for instructions and data.
Has a single bus for both instructions and data.	Has separate buses for instructions and data.
Has a single bus for both instructions and data.	Has separate buses for instructions and data.

Harvard Architecture



9



Von neumann architecture how does it work. What is the von neumann architecture.

noitcurtsni na dna dehctef eb ot snoitcurtsni eht fo sserdda eht sniatnoc taht retnuoc margorp a fo stsinoc tinU lortnoc eHT .seived tuptuo/tupni dna yromem ,JULA ekil stnenopmoc fo snoitarepo eht slortnoc metsys retupmoc a fo tinU lortnoc eHT tinU lortnoc .tuodeirrac eb ot Snoitarepo tinU cigolL dna citemhtrA .retsiger fo yteirav a dna JUC (tinU lortnoc) JULA tinU cigolL dna citemhtrA era UPC fo stnenopmoc rojam eht .retupmoc eht ni detaropronci era taht snoitcurtsni fo epyt eht yb detalcid snoitcurtsni fo yteirav a smrofrap UPC eHT . margorp retupmoc a fo snoitcurtsni eht gniutucexe rof elbispnoser tuicirc cirtcele na sa denifed eb osla nac tinU gnissecorp lartneC eHT .UPC eht sa ot derrefe si dna tinU gnissecorp lartneC eht deliac si snoitarepo gnissecorp atad fo klub eht smrofrap taht retupmoc eht fo trap eHT tinU gnissecorp lartneC tinU yromeM sesuB tinU gnissecorp lartneC ledom nnameN-noV fo stnenopmoc elyce etucexe-edoced-hctef eht gniwollof smargorp setuceXE .atad dna snoitcurtsni htob rof yromem eno sesU rossecorp elgnis a sesU .retupmoc desab-nnameN-noV A .yadot decudorp sretupmoc tsom ni desu lilti si ngised siHT .yromem emas eht ni derots era atad margorp dna atad noitcurtsni erehw .Ipecnoc retupmoc margorp-derots eht no desab si ertucehtra nnameN-noV .stuptuO/stupni dna sretsigerR ,JULA(tinU yromeM lacigolL dna .citemhtrA ,tinU lortnoc a fo detisnoc tl .ertucehtra nnameN-noV sa nwonk retal saw hcihw 5491 ni ngised ertucehtra retupmoc sih desoporp nnameN-noV .seroc fo rebmun eht dna deeps kcolc .ezis hcac no sdneped ecanamrofrap retupmoc .snoitcurtsni margorp SSECORP ot elyce etucexe-edoced-hctef eht gnisu sedulcni siht .ertucehtra nnameN-noV no desab era sretupmoc esoprup lareng tsoM modnaR(MAR :smetsys retupmoc ni desu era seiromem fo sepyt rojam owT .drow hcae ni stib fo rebmun eht dna sniatnoc ti sdrow fo rebmun eht yb deificeps si tinU yromem a fo ertucturts lanretni eHT .sdrow dellac stib fo spuorg ni noitamrofni yranib serots yromem eHT .egarots eht fo tuo dna ni noitamrofni refsnart ot dedeen stiucirc detaicossa htiw rehtegot sliec egarots fo noitcelloc a si tinU yromem A tinU yromeM .UPC eht morf sdnammoc/slangis seirrac suB lortnoc suB lortnoc .seived tuptuo/tupni eht dna tinu yromem eht . rossecorp eht neewteb atad seirrac suB atad suB atad .yromem eht dna rossecorp eht neewteb jatad eht ton tub(atad fo sserdda eht seirrac suB sserdda suB sserdda noitpirseD suB .refsnart atad rof smetsys sub rojam eerht fo desirpmoc ertucehtra nnameN-noV .refsnart retsiger Raluctrap hcae gginad sub eht yb detceles si retsiger hcihw enmreted slangis lortnoc .emit a ta enomom derrefsnart si noitamrofni yranib hcihw hguorht .retsiger a fo tib hcae rof eno .senil nomok o tes a fo stsinoc ertucturts sub A .metsys noitaruifnoc retsiger-elipitum a ni sretsiger eht neewteb derahs si noitamrofni hcihw yb snaeM eht era sesuB sesuB .gnissecorp gniurud noitcurtsni tneruc eht sniatnoc retsiger siHT)retsigerR noitcurtsni tnerruCU(RIC .detucexe eb ot noitcurtsni txeM eht fo sserdda eht sniatnoc retsiger siHT)retuocC(margorp CP .siluser cigolL dna citemhtra etaidemretni eht sdloh retsiger siHT)rotalumucca(CA .yromem morf ro ot derrefsnart gnieb si taht atad eht sdloh retsiger siHT)retsigerR atad yromeM(RDM .dessecca eb ot sdeen taht atad eht fo noitacol yromem eht sdloh retsiger siHT)retsigerR sserdda yromeM(RAM noitpirseD sretsigerR .gnissecorp atad ni elor laicurc a syalp taht sretsiger fo tsil eht si gniwollof .retsiger eht morf dehctef era UPC eht yb dessecorp atad eHT .UPC eht ni Saera areas deys-hghj ot refers sretsiger sretsiger .noitucexe romem morf dehctef era Memory) ROM (Read-Only Memory) Next TopicStore Program Control Concept View Discussion Improve Article Save Article Like Article Historically there have been 2 types of Computers: A Fixed Program Computers eAAA Their function is very specific and they couldnAAA be programmed, e.g. Calculators. A Stored Program Computers eAAA These can be programmed to carry out many different tasks, applications are stored on them, hence the name. A The modern computers are based on a stored-program concept introduced by John Von Neumann. In this stored-program concept, programs and data are stored in a separate storage unit called memories and are treated the same. This novel idea meant that a computer built with this architecture would be much easier to reprogram. A The basic structure is like, A It is also known as IAS computer and is having three basic units: A A The Central Processing Unit (CPU) A The Main Memory Unit A The Input/Output Device A LeteAAAs consider them in details. A A Control Unit eAAA A control unit (CU) handles all processor control signals. It directs all input and output flow, fetches code for instructions, and controls how data moves around the system. A Arithmetic and Logic Unit (ALU) eAAA A The arithmetic logic unit is that part of the CPU that handles all the calculations the CPU may need, e.g. Addition, Subtraction, Comparisons. It performs Logical Operations, Bit Shifting Operations, and Arithmetic operations. A Figure eAAA Basic CPU structure, illustrating ALU A Main Memory Unit (Registers) eAAA A Accumulator: Stores the results of calculations made by ALU. A Program Counter (PC): Keeps track of the memory location of the next instructions to be dealt with. The PC then passes this next address to Memory Address Register (MAR) A Memory Address Register (MAR): It stores the memory locations of instructions that need to be fetched from memory or stored into memory. A Memory Data Register (MDR): It stores instructions fetched memory or any data that is transferred and stored in, memory. The instruction that will not be executed immediately is placed in the IBR instruction buffer register. Input/output devices: the program or data is read in the main memory of the input device or secondary storage under the control of the CPU input instruction. Output devices are used to generate information from a computer. If the computer evaluates some results and is stored on the computer, then, with the help of output devices, we can present them to the user. The main internal components of the CPU and memory, through the buses. Types: DATA GOOD: It carries data between memory drive, E/S devices and processor. It carries CPU control commands (and other device status signals) to control and coordinate all activities within the computer. The instructions can only be done once at a time and can only be carried out sequentially. Both factors contain the competence of the CPU. This is commonly known as the "von Neumann bottleneck". We can provide a von Neumann processor with more cache, more RAM or faster components, but if you are going to get original gains on CPU performance, then an influential CPU configuration inspection should take place. This architecture is very important and is used in Our PC and even in Super Computers. design consists A of A Control Unit, Arithmetic and Logic Unit (ALU), Memory Unit, Registers and Inputs/Outputs. Von Neumann architecture is based on the A Stored-program computer concept, where instruction data and program data are stored in the same memory. A This design is still used in most A computers produced today. Central Processing Unit (CPU) The Central Processing Unit (CPU) is the electronic circuit responsible for executing the instructions of a computer program. It is sometimes referred to as the microprocessor or processor. The CPU contains the ALU, CU and a variety of registers. Registers Registers are high speed storage areas in the CPU. A All data must be stored in a register before it can be processed. Arithmetic and Logic Unit (ALU) The ALU allows arithmetic (add, subtract etc) and logic (AND, OR, NOT etc) operations to be carried out. Control Unit (CU) The control unit controls the operation of the computer. AAAs ALU, memory and input/output devices, telling them how to respond to the program instructions it has just read and interpreted from the memory unit. The control unit A Also provides the timing and control signals required by other computer components. Buses A Are the means by which A data is transmitted from one part of a computer to another, connecting all major internal components to the CPU and memory. A standard CPU system bus is comprised of a control bus, data bus and address bus. Address Bus Carries the A Addresses of data (but not the data) between the processor and memory. Data Bus Carries A data between the processor, the memory unit and the input/output devices. Control Bus Carries control signals/commands from the CPU (and status signals from other devices) in order A to A control and coordinate A all the activities within the computer. Memory Unit The memory unit consists of RAM, sometimes referred to as primary or main memory. A Unlike a hard drive (secondary memory), this memory is fast and also directly)mrof yranib ni Htob(Stnetnoc sti dna sserdda na Fo ssintical hcae a , .snoitcilla a a .otni tilps Si Mar. Upc Eht YB

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